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# A NANO QUIESCENT CURRENT POWER MANAGEMENT FOR AUTONOMOUS WIRELESS SENSOR NETWORK

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## ABSTRACT

A Nano quiescent current Power Management for Autonomous Wireless Sensor Network dedicated to indoor and outdoor Photovoltaic (PV) Energy Harvesting is presented. The standby current consumption is 130nA @ 2.4V. The battery can be charged with less than 5% of the harvested energy from low light (70 lux) to high light (1 sun). The IP starts charging at light level as low as 10 lux providing  $\simeq 1\mu W$ . The voltage range is 2.4V - 3.2V. The system load can be powered up to 400mA. This power management is manufactured using the CMOS 90nm technology in a  $0.4mm^2$  die area.

**Index Terms**— power management, wireless sensor network, energy harvesting, ultra-low power consumption.

## I. INTRODUCTION

Autonomous Wireless Sensor Network (AWSN) products are becoming possible thanks to the progress of ultra-low power techniques in microelectronics leading to energy consumption reduction. One of the goal of AWSN is to avoid the use of disposable batteries, making it more *eco-friendly*. Another goal is to extend the system life time. In an AWSN node, the system load is composed of a micro-controller, one or several radios, and one or several sensors. Most of the time, this system consumes a greater current than the harvested one [1]. So, the system has to be duty cycled and a rechargeable battery (or alternatively a supercap) has to be used as an energy tank. Thus, the battery is recharged by the harvester when the system is sleeping. The AWSN is viable when the energy levels are balanced between the quantity of harvested energy (including when the system is sleeping) and the energy consumed by the system (in active mode but also leakage and eventual counters inactive periods).

When the harvested energy is in the range of a few microwatts, the power management is not allowed to spend a large

part of this energy. In this case, the use of hungry power converters is prohibited.

In [2] a switched capacitor converter is used to convert the harvested energy to the load. This architecture can be fully integrated but no more than 50% of the input energy can be converted. In [3] a converter is needed to boost up the input voltage of the PV cell to charge the battery. These kinds of architectures are required when the harvester is working in a different voltage range than the battery. In [4] the circuit consumption is very low and the power management is used to power or standby the system from the battery when required. But this device is working only from battery to the system and doesn't manage or protect the battery. In [5], an inductor based converter is used to convert photovoltaic energy harvested from indoor. The best end to end efficiency is 70% while consuming  $0.8\mu A$  of static current. In [6], the charger uses a boost converter to charge a 4.2V battery but also requires a low-dropout regulator to power the system load if working at 3V. The use of 2 converters to power a load decreases the global efficiency. In [7] the power management is composed of an inductor based boost converter (DC/DC). With this converter, the input voltage cannot be equal or higher than the system voltage. This DC/DC powers directly the system and a battery or super-capacitor is used as an energy buffer. The buffer is protected against over and under voltage, but it's not protected against overcharging current. Some kinds of battery can be damaged with a high harvested current [8]. With this application, the voltage of the harvester should always be under the battery voltage. The reported power management quiescent current is at 330nA. A coil and other external components are required for system configuration.

In our proposal, the consumption of the power management is below 5% of the harvested current thanks to different working modes. These modes are autonomously managed. The three parts (the harvester, the battery and the system load) are wisely chosen to work in the same voltage range.

In this way, the efficiency can reach 87%. A Lithium-Ion battery charger, essential to protect the battery against high charging current, is also implemented.

This paper is presented in 5 sections: the circuit architecture will be presented in section II, the efficiency calculations are described in section III, the experimental results in section IV and the conclusion in section V.

## II. POWER MANAGEMENT ARCHITECTURE

The power management (PM) has been designed to work with commercial batteries and PV. These parts are entirely part of the system. The photovoltaic cells are working from low light up to high light and are well suitable to work for indoor applications. We consider we will get a better efficiency without a hungry Maximum Power Point Tracking (MPPT) system.

Fig. 1 shows the block schematic of the power management. It can be supplied either by a 5V external power (i.e. USB) or by any source of power, in the range 0-6V. Depending on the light condition and the system current requirement, the PV can power the system and/or recharge the battery. This PM, supports batteries working in the range 2-3.2V. It embeds protection against over-voltage to avoid Li-Ion batteries over-charging. For this purpose, we use a Low Drop-Out (LDO) regulator that regulates the voltage at 3.2V. The under-voltage battery protection, designed to avoid the battery to be undercharged and be damaged, is designed with an hysteresis comparator that always senses the voltage of the battery. It will turn off the Power Switch (PS) when the battery is discharged to 2.4V. This will avoid the battery to be deeply discharge under 10% and then increase the battery life time (Lithium-Ion battery requirements). The comparator will turn PS when it is recharged to 2.8V.

We define different working modes to adapt to the light conditions and provide the best efficiency for the minimal power cost. The *dark* mode is the lowest power consumption mode. When no light is present, the system is powered only by the battery. The S2 switch, commanded by a resistor ladder that senses the PV voltage, is open to avoid leakage from battery through the PV\_LDO. The current flows from the battery through the Fast Switch (FS) that is switched on and through the switch PS. (S3 is consequently open). Only the battery protection and some very low power digital circuitry consume a part of the battery energy. The total consumption of the PM is 130nA at 2.4V.

The second mode is the *low-light* mode: from a few tens of lux to around 3klux. A voltage is present at the input of the PV\_LDO, the output of the ladder is higher than the threshold of the switch, S2 is then turned-on. The PV\_LDO is in *follower* mode, meaning that it is not regulating, the voltage drop is low ( $V_{OUT} \simeq V_{IN}$ ) and it provides all the energy harvested by the PV to the battery and/or the system. It also drives S3 (open) and FS (closed). This command occurs when the PV current capability is below the required

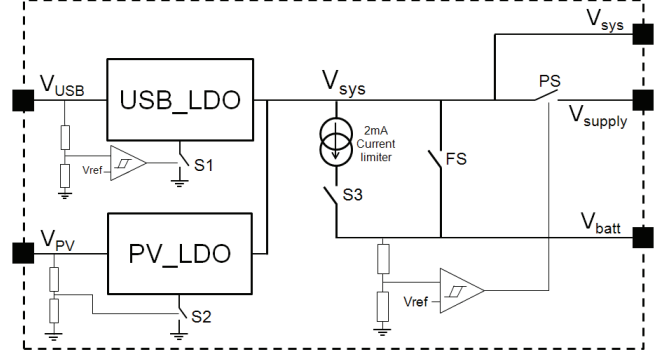


Fig. 1: Block schematic.

load current. The 2mA current limiter is off, saving the power and the battery is recharged by FS. The battery can also power the system through the switch PS. In this mode, the PV\_LDO consumes only 315nA at 150lux from the PV, which represent 1.75% of the harvested current. The total Power Management consumption is less than 580nA at 2.4V.

The third mode is the *high-light* mode. When the current from PV reaches a threshold fixed at 800 $\mu$ A the PV\_LDO switches in *regulator* mode, consuming 5 $\mu$ A, which represent 0.625% of the harvested current. The LDO regulates the internal common rail at 3.2V avoiding the battery to be overcharged. It switches on S3 and off FS. The 2mA limiter is active, consuming  $\frac{I_{charge}}{1000}$ . If the system requires a peak current higher than the PV can provide, FS switch is immediately closed in order to provide the required current. This is achieved by sensing  $V_{sys}$  and the voltage of the battery. Once  $V_{sys} < V_{batt}$  (which means high current is required), the FS is active. It will be open when the system goes back to sleep. ( $V_{sys} > V_{batt}$ )

The last mode is the *full-power* mode. This mode occurs only when an external power is connected to the USB pad. As soon as a voltage is applied on it, S1 is switched on by the comparator and the USB\_LDO turns on and off the current limiter and the FS switch respectively and forces the PS switch whatever the battery voltage.

## III. EFFICIENCY CALCULATION

The PV used for the design is working in the same voltage range than the battery. According to [9], the efficiency that we can have from a PV depending on the maximum power point and the effectively operating point is defined as:

$$\eta_{PV} = \frac{P_{transferred}}{P_{MPP}} \quad (1)$$

$$P_{MPP} = V_{MPP} \times I_{MPP} \quad (2)$$

Depending on the current/voltage (IV) curves of the PV cell we are using, we can compute the power loss (or the efficiency) of the harvested power depending on the

Maximum Power Point (MPP) and the Operating Point (OP), defined by  $V_{batt}$ .

The figure 2 presents a typical measured current/voltage (IV) and power/voltage (PV) curve and the position of the MPP and OP on the Power vs Voltage (PV) curve. The table I presents the efficiency of the transferred energy for different light conditions at different battery voltages:

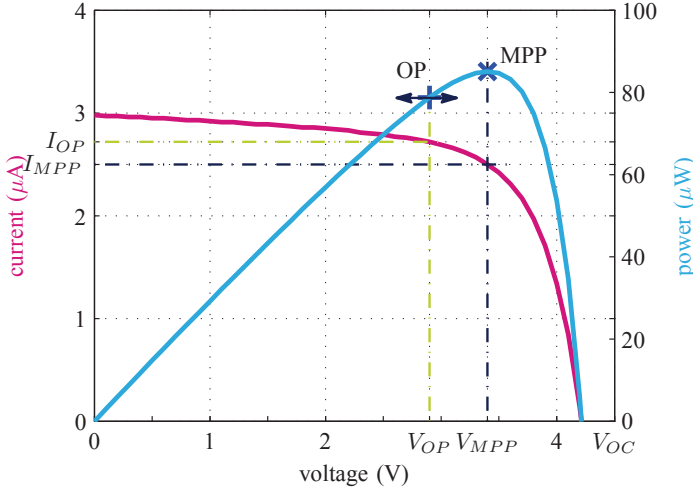


Fig. 2: IV and PV curve.

$V_{batt}$ lux	2.4	2.6	2.8	3
20	100%	96%	81.9%	47 %
200	90%	94.8%	97.6%	100%
800	86%	91.5%	94.5%	98.6%
2000	84%	88.4%	91%	96%

Table I: Transferred energy on PV.

The efficiency of this kind of power management is the ability to transfer the power. If the input power is entirely transferred to the output, the efficiency will be 100%. In other case, if there is a loss due to the joule effect, the efficiency will be:

$$\eta_{PM} = \frac{P_{OUT}}{P_{IN}} \quad (3)$$

The efficiency of the power management has been computed for different values in the table II:

$V_{batt}$ lux	2.4	2.6	2.8	3
20	76.5%	75.6%	72.8%	71.8%
200	85.6%	85.4%	85.6%	86.4%
800	84.2%	85%	85.8%	86.6%
2000	83%	84.4%	85.4%	86.3%

Table II: PM efficiency.

The global efficiency is calculated as the efficiency of the power management by the efficiency of the OP vs MPP:

$$\eta = \eta_{PV} \times \eta_{PM} \quad (4)$$

$$\eta = \frac{P_{transferred} \times P_{OUT}}{P_{MPP} \times P_{IN}} \quad (5)$$

With this equation, we can compute the global efficiency from the previous tables. Different values are presented in the table III:

$V_{batt}$ lux	2.4	2.6	2.8	3
20	76.5%	72.5%	59.6%	33.7%
200	77%	81%	83.5%	86.4%
800	72.4%	77.7%	81%	85.3%
2000	70%	74.6%	77.7%	82.8%

Table III: Global efficiency.

#### IV. EXPERIMENTAL RESULTS

Simulations and chip measurements are matching and the power management has been tested in a global wireless sensor node. Table IV compares the power management consumptions between the simulations and measures for the tree harvesting modes:

light condition	$I_{PM}$	simulations	measurements
dark		156nA	130nA
low light		572nA	500nA
high light		5.3μA	5.2μA

Table IV: Simulations vs Measurements.

The figure 3 shows the 3 modes of the power management: the *dark* mode, when the PV provides no current, the *low-light* mode where the current from PV goes directly through the battery and the *high-light* mode, the most power hungry mode, with regulation and current limiter for battery charging:

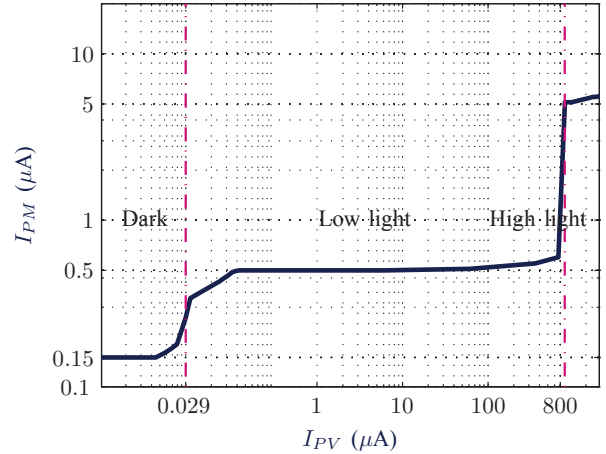


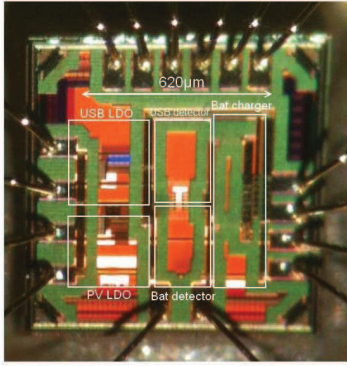
Fig. 3: PM consumption vs PV current.

The table V presents the comparison of our solution versus the other published solutions :

	[5]	[6]	[7]	This work
quiescent current	650 nA	625 nA	330 nA	130 nA
overcharging current protection	NO	NO	NO	YES
Power throughput	5 $\mu$ ~ 10 mW	1 $\mu$ ~ 0.1 W	10 $\mu$ ~ 0.3 W	1 $\mu$ ~ 0.5 W
input charging voltage	0.5 ~ 2 V	0.8 ~ 5.7 V	0.13 ~ 3 V	$V_{batt} \sim 6 V$
process	250 nm 11 mm <sup>2</sup>	NA	NA	90 nm 0.4 mm <sup>2</sup>

**Table V:** Comparison of this work with recently published work.

The fig. 4 is the micrograph of the chip fabricated in 90nm with an effective area of 0.4mm<sup>2</sup>:



**Fig. 4:** Micrograph of the Power Management in CMOS 90nm.

## V. CONCLUSION

In this paper, a power management system from photovoltaic harvester to battery and to system load is presented.

Typical commercial power management doesn't include battery protection for charging current. Most of rechargeable lithium batteries are suitable for wireless sensors but require careful attention for charging. Our solution propose an area-efficient complete system power management for very low-energy harvesting. With a suitable battery and PV, this PM is convenient for AWSN. The measurements show very low power consumption with less than 130nA standby current in a tiny 0.4mm<sup>2</sup> die area. The efficiency is higher than 85% from 30 $\mu$ A of the harvested current (corresponding to 200 lux: table II).

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